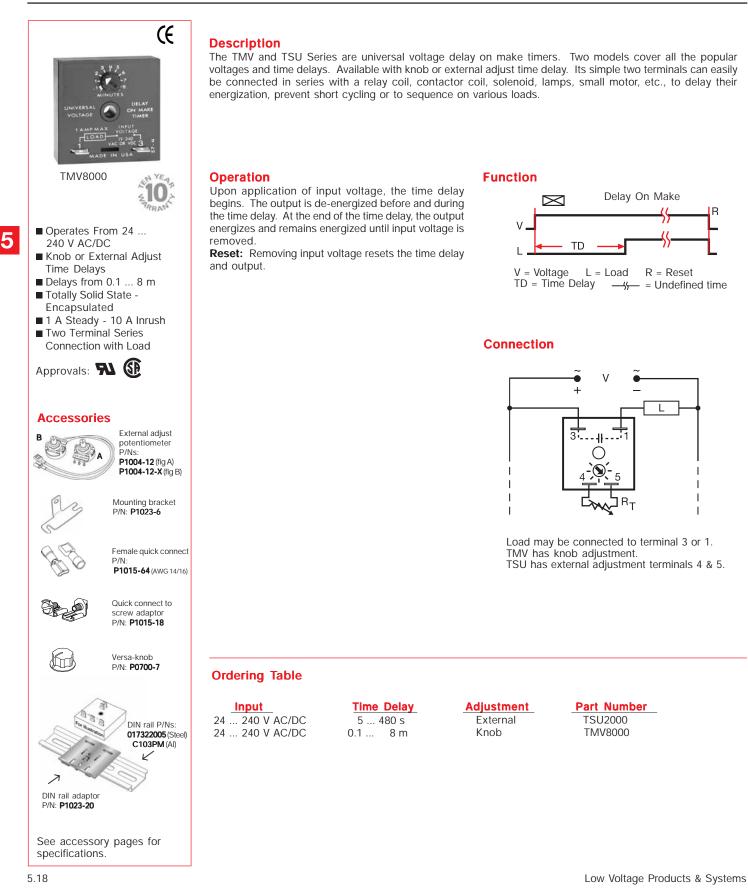
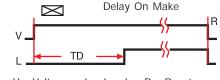
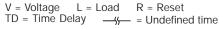
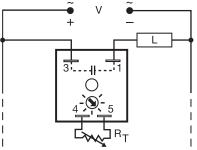
Delay On Make (Operate) TMV8000/TSU2000 Uni-Timer **Timing Module**









Load may be connected to terminal 3 or 1. TSU has external adjustment terminals 4 & 5.

Delay On Make (Operate) TMV8000/TSU2000 Uni-Timer Timing Module

Technical Data

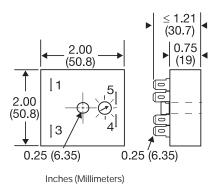
Time Delay Type Range Repeat Accuracy Tolerance (Factory Calibration) Reset Time	Analog circuitry 5 480 s (TSU2000) 0.1 8 m (TMV8000) +/-2% ≤ +/-10% ≤ 100 ms
Input Voltage Line Frequency	24 240 V AC/DC +/-20% 50 60 Hz
Output Type Form Maximum Load Current Minimum Holding Current Voltage Drop	Solid State Normally Open, open during timing 1 A steady state, 10 A inrush at 55°C ≤ 40 mA ≅ 2.5 V at 1 A
Protection Circuitry Dielectric Breakdown Insulation Resistance	Encapsulated \geq 2000 V RMS terminals to mounting surface \geq 100 M Ω
Mechanical Mounting Package Termination	Surface mount with one #10 (M5 x 0.8) screw 2 x 2 x 1.21 in. (50.8 x 50.8 x 30.7 mm) 0.25 in. (6.35 mm) male quick connect terminals
Environmental Operating Temperature Storage Temperature Humidity Weight	-20°C +70°C -30°C +85°C 95% relative, non-condensing ≅ 2.4 oz (68 g)

TSU2000

R _T Selection Chart		
Time Delay*		
Seconds	R _T	
00001103	Megohm	
5	0.0	
85	0.5	
163	1.0	
240	1.5	
320	2.0	
400	2.5	
480	3.0	

* When selecting an external R_T add at least 20% for tolerance of unit and the R_T.

Mechanical View



TMV has the knob and dial. TSU has terminals 4 & 5.

Low Voltage Products & Systems

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